(11) **EP 0 818 129 B1**(12) **EUROPEAN PATENT SPECIFICATION**

(45) Date of publication and mention
of the grant of the patent:
18.06.2003 Bulletin 2003/25

(51) Int Cl.7: **H05B 41/295**(21) Application number: **96938661.4**(86) International application number:
PCT/US96/17281(22) Date of filing: **28.10.1996**(87) International publication number:
WO 97/024016 (03.07.1997 Gazette 1997/29)(54) **CONTROL AND PROTECTION OF DIMMABLE ELECTRONIC FLUORESCENT LAMP BALLAST
WITH WIDE INPUT VOLTAGE RANGE AND WIDE DIMMING RANGE****STEUERUNG UND ÜBERWACHUNG VON DIMMBAREN VORSCHALTGERÄTEN MIT BREITEM
BELEUCHTUNGSHUB****COMMANDE ET PROTECTION DE BALLAST POUR LAMPE FLUORESCENTE ELECTRONIQUE
POUVANT ETRE ATTENUÉE A PLAGE ÉTENDUE DE TENSION D'ENTRÉE ET A PLAGE
ÉTENDUE D'ATTENUATION**(84) Designated Contracting States:
CH DE FR GB LI

- **STEIGERWALD, Robert, Louis**
Burnt Hills, NY 12021 (US)
- **KHERALUWALA, Mustansir, Hussain**
Schenectady, NY 12309 (US)

(30) Priority: **26.12.1995 US 578729**(43) Date of publication of application:
14.01.1998 Bulletin 1998/03

(74) Representative: **Goode, Ian Roy et al**
London Patent Operation
General Electric International, Inc.
Essex House
12-13 Essex Street
London WC2R 3AA (GB)

(73) Proprietor: **GENERAL ELECTRIC COMPANY**
Fairfield, CT 06431 (US)

(72) Inventors:

- **STEVANOVIC, Ljiljana, Dragoljub**
Clifton Park, NY 12065 (US)

(56) References cited:

EP-A- 0 338 109	EP-A- 0 391 383
EP-A- 0 435 231	WO-A-96/03017

Note: Within nine months from the publication of the mention of the grant of the European patent, any person may give notice to the European Patent Office of opposition to the European patent granted. Notice of opposition shall be filed in a written reasoned statement. It shall not be deemed to have been filed until the opposition fee has been paid. (Art. 99(1) European Patent Convention).

EP 0 818 129 B1

1

EP 0 818 129 B1

2

Description

Field of the Invention

[0001] The present invention relates generally to fluorescent lamps and, more particularly, to high-frequency electronic dimming ballasts therefor.

Background of the Invention

[0002] Typical modern dimmable fluorescent lamp systems include a high-frequency resonant ballast inverter operating from a dc input voltage source and supplying approximately sinusoidal high-frequency ac current to the fluorescent lamp(s). The dc input voltage to the ballast is typically obtained by rectifying an ac utility voltage or, alternatively, directly from a dc source such as a battery. By operating the resonant ballast inverter above its natural resonance, the output characteristics of the inverter resemble an ideal ac current source, and the dimming function is achieved by increasing the inverter switching frequency which, in turn, reduces the lamp current. In addition to regulating light output of the lamp, the ballast inverter also provides high output voltage for starting the lamp(s) having an amplitude which is usually two to three times higher than the lamp voltage during normal operation. A ballast system generally in accordance with the preamble of claim 1 hereof is described in EP-A-0,338,109.

[0003] To maximize efficiency, it is desirable to have zero-voltage switching (ZVS) operation of the inverter switching devices which greatly reduces switching losses. (ZVS is switching a device with zero voltage thereacross.) On the other hand, if the same resonant inverter is operated below resonance, ZVS operation is lost and the devices of the inverter are hardswitched, resulting in relatively high switching losses. Therefore, in high-frequency ballasts with a wide dimming range, i.e., 100% to about 1 % full light output, it is desirable to enable ZVS operation throughout the entire dimming range. It is also desirable to maintain ZVS operation and the full dimming range with a wide input dc voltage range, e.g., two-to-one. Still further, it is desirable to achieve the aforementioned features over a relatively narrow switching frequency range in order to lower switching losses, reduce likelihood of interference with other equipment, and to reduce resonant switching stresses.

[0004] Accordingly, it is desirable to provide a control for a dimmable fluorescent lamp ballast which maintains operation above resonance over a wide dc input voltage range and throughout the entire dimming range, while maintaining a relatively narrow switching frequency range. In addition, because ballast inverter operation below resonance results in relatively high switching losses and could cause overheating and even failure of switching devices, it is desirable to have a protective feature for detecting and preventing prolonged operation of the ballast below resonance. The large amplitude

of the output voltage waveform during starting is also potentially damaging to resonant components at the output of the ballast inverter such that some type of overvoltage protection is desirable. Such overvoltage protection should not interfere with normal starting and running operation of the ballast, but should prevent the output voltage from exceeding a predetermined value by shutting down operation of the inverter. (It should be noted that during ZVS operation, the resonant ballast inverter is immune to a short circuit condition at the output, making it unnecessary to protect a system operating with ZVS against this failure mode.)

Summary of the Invention

[0005] According to the present invention, there is provided a ballast system in accordance with claim 1 hereof.

[0006] A ballast system for at least one dimmable fluorescent lamp comprises a resonant switching inverter and a controller therefor which controls the inverter to operate above resonance during starting and normal running operation. The controller preferably comprises a pulse width modulation (PWM) regulator. After a start delay timer allows time for the lamp filaments to heat up, the PWM regulator provides control signals to a gate driver to drive the switching devices of the switching inverter initially at a relatively high frequency (i.e., higher than a normal operating frequency for the lamp) and then reduces the frequency until a sufficiently high voltage is reached to start the lamp. Once the lamp is started, the inverter is operated in its normal feedback mode to feed back sensed lamp current signals to the PWM regulator which compares the sensed lamp current signals with a commanded current signal.

[0007] The ballast system further comprises an overvoltage shutdown mechanism with two different threshold levels. During lamp starting, if the output voltage is greater than a first overvoltage shutdown threshold, then an overvoltage shutdown timer is activated to shut down operation of the inverter for a predetermined overvoltage shutdown period. After the lamp has started, a second overvoltage shutdown threshold is activated which is lower than the first overvoltage threshold for avoiding operation of the inverter below resonance.

[0008] The gate driver circuit control is operated to provide gating signals with a constant OFF-time, variable ON-time. In this way, both the frequency and duty cycle of the square wave voltage waveform at the input of the inverter resonant circuit are variable to achieve a wide dimming range over a relatively narrow frequency span. Advantageously, the narrow frequency range results in lower switching losses, a reduced likelihood of interference with other equipment, and reduced stresses on resonant circuit components.

3

EP 0 818 129 B1

4

Brief Description of the Drawings

[0009] The features and advantages of the present invention will become apparent from the following detailed description of the invention when read with the accompanying drawings in which:

FIG. 1 schematically illustrates a ballast system in accordance with the present invention; and

FIG. 2 graphically illustrates frequency response for a ballast system such as that of FIG. 1.

Detailed Description of the Invention

[0010] FIG. 1 schematically illustrates a fluorescent lamp system in accordance with the present invention. By way of example, FIG. 1 illustrates a two-lamp system with fluorescent lamps 12 and 22. It is to be understood, however, that the present invention applies to fluorescent lamp systems having one or more lamps. The ballast inverter 10 is shown schematically as comprising a conventional half-bridge resonant switching ballast configuration for a fluorescent lamp. A capacitor C1, typically electrolytic, is coupled across the ballast input in order to provide a rectified, filtered dc voltage to a half-bridge connection of switching devices Q1 and Q2. This input is typically obtained from rectifying an ac utility voltage. Alternatively, it can be obtained directly from a dc source such as a battery. A PWM regulator 11 provides gate drive signals to gate driver circuit 13 for alternately switching devices Q1 and Q2 to provide bi-directional current flow through a resonant circuit, including an inductor L1 and a capacitor C4, which is shown as being coupled through an output transformer To and a capacitor C2 to the junction between the switching devices Q1 and Q2. The series-connected lamps 12 and 22 are connected across series-connected capacitors C3 and C4. The capacitor C3 is used to extend the dimming range of the ballast by changing the resonant characteristics of the resonant circuit after the lamp starts. A resistor R3 is provided as a current sensor for controlling the lamp dimming function in a manner well-known in the art. A starting capacitor C5 is connected between the junction joining the lamps and ground, which momentarily shorts the lamp 22 so that a higher voltage is applied to the lamp 12 for starting.

[0011] FIG. 2 shows a family of frequency response curves for an exemplary ballast inverter system such as that of FIG. 1. Each curve represents output voltage Vo as a function of frequency of input voltage Vac and with a total lamp resistance Rlamp as a changed parameter. Input voltage amplitude was constant. To achieve high-efficiency operation, it is desirable to operate the lamp(s) during starting and running above the resonant frequency fr, i.e., on the right side of the peak of the respective frequency response curve. Operation above the resonant frequency allows for ZVS of the active de-

vices of the ballast inverter and hence higher efficiency.

[0012] It is important to note that the resonant frequency fr of the inverter shown in FIG. 1 changes with loading as can be seen by looking at the peaks of the resonant curves in FIG. 2. This is easy to understand by examining two extreme load cases in FIG. 1. During starting, before the lamps are ignited, the impedance of both lamps 12 and 22 is essentially infinite, and the self-resonant frequency of the resonant inverter is approximately

$$f_{r1} = \frac{1}{2\pi\sqrt{L1C4}}$$

When the lamps are illuminated and at full brightness, the impedance of both lamps is much lower than impedance of the capacitor C3 at the operating frequency, and the self-resonant frequency of the resonant inverter can be approximated as

$$f_{r2} = \frac{1}{2\pi\sqrt{L1(C4+C3)}}$$

As the lamps are dimmed, their impedance increases and the loading of the resonant inverter changes, also changing its resonant frequency. This is clearly shown in FIG. 2, where the load resistance parameter is changed from Rlamp = 550Ω, representing the resistance of two lamps at full brightness, to Rlamp3 = 8kΩ, which is approximately the resistance of two lamps at 5% brightness.

[0013] In accordance with the present invention, a high-frequency electronic ballast system for driving one or more fluorescent lamps includes a starting control for starting the lamp(s) at a frequency above the resonant frequency fr2, an overvoltage protection mechanism during both starting and normal operation, and a dual voltage control for operating the lamp over a wide dimming range.

[0014] In operation of the ballast system of FIG. 1, lamp current Iac is sensed by the sensing resistor R3 and is provided to the PWM regulator via an absolute value circuit 24. (Although the resistor R3 is provided for sensing lamp current, any other device suitable for providing a signal indicative of light output may be employed.) An overvoltage protection circuit 30 comprises a switching device Q3 in series with a resistor R4 connected between the junction a joining resistors R1 and R2 and ground. The junction a is connected to the non-inverting input of an overvoltage shutdown (OVSD) comparator 32. The inverting input of the comparator 32 is connected to a Zener diode 34. The output of the comparator 32 is provided as an input to an overvoltage shutdown timer 36 which in turn provides an input to the gate driver circuit 13. The output of the timer 36 is also provided to the PWM regulator 11 via the series connec-

5

EP 0 818 129 B1

6

tion of diodes D 1 and D3 and a resistor R5. The output of a start delay timer 40 is also provided as an input to the PWM regulator 11 via diodes D2 and D3 and the resistor R5. The cathodes of diodes D1 and D2 are connected together at a junction *b* which is coupled to the gate of the switching device Q3 via a diode D4 and an RC delay circuit 41.

[0015] To start the lamps, the capacitor C5 momentarily shorts the lamp 22 so that a higher voltage is applied to the other lamp 12 for starting. In addition, the start delay timer 40 delays starting the lamps until after the start delay timer 40 times out in order to allow the lamp filaments (which are supplied by a separate power supply (not shown)) to heat up before actually starting the lamps so that minimum sputtering of the filament emission mix occurs as the lamp discharge current begins to flow. FIG. 1 illustrates an exemplary start delay timer which provides an approximately 3.5 second time delay.

[0016] The PWM regulator 11 is operated to start at a predetermined relatively high frequency, i.e., higher than the open circuit resonant frequency f_{r2} , and "sweep down" through lower frequencies causing the output voltage V_o to build up as the peak of the resonant curve is approached (see resonant curve 42 of FIG. 2). When the voltage V_o is high enough to break down the discharge into the initial visible stages of ionization (this starting voltage amplitude is usually between 400-700V for a two-lamp ballast, depending on the ambient temperature), the discharge current is established. Such operation may be explained graphically with reference to FIG. 2 wherein operation moves from a curve (e.g., 42) representing highly unloaded, i.e., the high impedance case before the lamps are started, to operation on a loaded curve (e.g., 43, 44, 45) where the lamps are running. This can also be seen by looking at the curve 46 of FIG. 2 which approximately shows the loci of all lamp operating points from full light output (operating point at 53 kHz) to approximately 5% light output (at 70 kHz). Each operating point on the curve 46 is determined by intersection of the inverter output characteristic (resonant circuit frequency response) for that particular output load (curves 42 through 45) and the lamp voltage-current characteristic (not shown). It is important to note that during both starting and running operation throughout the entire dimming range, the loci of lamp operating points always remain on the right-hand side of the resonance of the corresponding resonant curve (42 through 45), resulting in above resonance operation of the ballast inverter, hence allowing for ZVS.

[0017] The ballast system according to the present invention also comprises an overvoltage shutdown mechanism including a circuit comprising the switching device Q3 and resistor R4. The device Q3 is initially on because the output of the start delay timer is high. The voltage at junction *a*, i.e., the output of the R1-R2 voltage divider, is provided to the comparator 32 to determine if the output voltage V_o is greater than a first overvoltage

threshold V_{OVSD1} . If so, then the overvoltage shutdown timer 36 is activated to turn off the switching devices of the ballast inverter 10 via the gate driver 13 for an overvoltage shutdown period, e.g., shown in FIG. 1 as being 2.5 seconds. After the overvoltage shutdown period, the PWM regulator starts sweeping down again from the initial predetermined high frequency to start the lamps as described hereinabove.

[0018] Hence, if either V_o (as sensed by the voltage divider R1 and R2 in parallel with R4) is greater than the overvoltage shutdown threshold voltage or the start delay timer output signal is high, then device Q3 is on and the lamp current sensed by resistor R3 is overridden through the diode-OR arrangement of diodes D1 and D2, such that the PWM regulator 11 operates to sweep down through the frequency range as described hereinabove.

[0019] As an additional protective feature of the overvoltage shutdown scheme, the ballast system according to the present invention distinguishes between operation above the resonant frequency and operation below the resonant frequency. In particular, there is a second overvoltage shutdown voltage threshold V_{OVSD2} which is lower than the first overvoltage shutdown voltage threshold V_{OVSD1} , i.e., $V_{OVSD2} < V_{OVSD1}$. The lower threshold V_{OVSD2} is activated only after the lamps are on and running normally by turning off Q3, thus increasing the gain of the voltage divider (a larger fraction of V_o is now fed to comparator 32 via the R1-R2 voltage divider). The reason two threshold levels are needed is that the upper threshold V_{OVSD1} is not low enough to prevent lamp operation below resonance. For example, if a lamp were to fail during running operation, then the output voltage would increase but not enough to trip the first overvoltage level V_{OVSD1} . However, with the second overvoltage threshold V_{OVSD2} enabled after a startup interval, once either of the lamps fails, the output voltage reaches the second overvoltage threshold, activating the overvoltage shutdown timer 36 and the gate driver 13 is shut down and the starting process begins again. Otherwise, without this additional protective feature afforded by the second overvoltage threshold, the control, which would be operating below resonance, would seek to raise the lamp current by lowering the frequency and thus would stay below resonance so that ZVS would not be possible, losses would be high and there would be a danger of damaging the inverter switching devices.

[0020] After the lamps start such that the start delay timer output is low and the overvoltage shutdown timer output is low, the ballast system operates in its normal feedback mode, i.e., sensing the lamp current with the resistor R3 and providing the lamp current as an input through the absolute value circuit 24 to the PWM regulator 11. Note that once the lamps have been started, the output of the overvoltage shutdown comparator 32 does not go low until after a time delay determined by the RC circuit 41 which may be on the order of, for example, 100 ms to ensure that the lamps are on before

7

EP 0 818 129 B1

8

the second overvoltage threshold is activated.

[0021] During normal running operation, the PWM control scheme implemented in this ballast inverter design has a two-fold effect on the output voltage V_o . In particular, the gate driver 13 is controlled by the PWM regulator 11 to provide a constant OFF-time for the inverter switching devices Q1 and Q2, but to provide a variable ON-time. Suitable PWM regulators 11 are, for example, devices numbered UCX862 and UCX864 manufactured by Unitrode Corporation. By keeping the OFF-time constant and varying the ON-time for the inverter switching devices, both the frequency and duty ratio of the squarewave voltage V_{SQWV} at the primary of transformer T_o are varied. The variation in switching frequency affects the ballast inverter output voltage V_o as shown in FIG. 2. The variation of duty ratio of the ac voltage V_{SQWV} has an added influence on the inverter output voltage V_o . At the minimum operating frequency of the ballast inverter, the PWM regulator is set up to operate with approximately equal ON-time and OFF-time, resulting in 50% duty ratio of switching devices Q1 and Q2. This makes the amplitude V_{ac1} of the fundamental component of the V_{SQWV} voltage highest for a given dc input voltage V_{DC} . When the lamps are dimmed, the ballast inverter frequency is increased by reducing the ON-time of the PWM regulator, and the duty ratio of the two switching devices Q1 and Q2 is no longer 50%. (The duty ratio of Q1 is lower than 50%, while the duty ratio of Q2 is higher than 50%.) As a result, the fundamental component V_{ac1} of the voltage V_{SQWV} has lower amplitude than at 50% duty ratio, all else being equal. Since the input voltage v_{ac} to the resonant circuit of FIG. 1 is proportional to the fundamental component V_{ac1} of the transformer primary voltage V_{SQWV} (the constant of proportionality being the transformer turns ratio), it is apparent that a change in the duty ratio of the switching devices directly affects the amplitude of the transformer secondary voltage v_{ac} and output voltage V_o . The combined effect of the frequency variation and the duty ratio variation is control of the output voltage to achieve full dimming range over a relatively narrow frequency range. Advantageously, the narrow frequency range results in lower switching losses, reduced likelihood of interference with other equipment, and reduced resonant component stresses. In addition, operation in the ZVS mode for any lamp operating mode (including lamp failures) is guaranteed by virtue of the dual overvoltage threshold level circuitry.

Claims

1. A ballast system for at least one dimmable fluorescent lamp (12,22), comprising:

a ballast inverter (10) for driving said at least one dimmable fluorescent lamp to provide light output, said ballast inverter comprising a reso-

nant switching inverter (L_1, C_4) for providing an output voltage for operating said lamp;
a gate driver circuit (13) for providing gating signals to said resonant switching inverter over a frequency range;

an inverter controller (11) for providing control signals to said gate driver circuit for controlling resonant operation of said ballast inverter to start the lamp by sweeping through an operating frequency range beginning with a predetermined relatively high initial frequency and continuously reducing the operating frequency until said output voltage is sufficiently high to start said lamp; and

an overvoltage shutdown circuit coupled (32,36) to said lamp for shutting down said inverter for an overvoltage shutdown period whenever said output voltage exceeds an overvoltage shutdown threshold, said inverter controller again providing control signals for sweeping through the operating frequency range beginning with the predetermined relatively high initial frequency to start the lamp after the overvoltage shutdown period; **characterized by**

said inverter controller providing control signals to said gate driver circuit for maintaining operating above resonance after starting the lamp such the said ballast inverter operates in a substantially zero-voltage switching mode.

2. The ballast system of claim 1 wherein said overvoltage shutdown threshold comprises a first overvoltage shutdown threshold during lamp starting and a second overvoltage shutdown threshold during normal running operation, said first overvoltage shutdown threshold being greater than said second overvoltage shutdown threshold in order to enable lamp starting and to avoid operation below resonance.
3. The ballast system of claim 1, further comprising a sensing device (R_3) for providing a signal indicative of light output from said lamp.
4. The ballast system of claim 3 wherein said sensing device comprises a current sensor for sensing current in said lamp.
5. The ballast system of claim 4 wherein said inverter controller comprises a PWM regulator circuit for receiving an input current command (i^*) and comparing the sensed lamp current therewith.
6. The ballast system of claim 1, further comprising a start delay timer circuit (40) for delaying starting of said lamp until sufficient time has elapsed to allow for lamp filament heating.

9

EP 0 818 129 B1

10

7. The ballast system of claim 1 wherein said overvoltage shutdown circuit comprises a switching device (32) for changing the gain of the output voltage sensing circuit.
8. The ballast system of claim 1 wherein the OFF-time of said gating signals is constant, the ON-time of said gating signals being variable in order to vary the frequency and amplitude of the output voltage from said switching inverter.

Patentansprüche

1. Vorschalt- bzw. Ballastanordnung für wenigstens eine dimmbare Leuchtstofflampe (12, 22), enthaltend:

einen Ballast-Wechselrichter (10) zum Speisen wenigstens einer dimmbaren Leuchtstofflampe für eine Lichtabgabe, wobei der Ballast-Wechselrichter ein Schwingenschalt-Wechselrichter (L1, C4) aufweist zum Liefern einer Ausgangsspannung zum Betreiben der Lampe, eine Gate-Treiberschaltung (13) zum Liefern von Steuersignalen an den Schwingenschalt-Wechselrichter über einem Frequenzbereich, eine Wechselrichter-Steuerung (11) zum Liefern von Steuersignalen an die Gate-Treiberschaltung zum Steuern des Schwingbetriebs des Ballast-Wechselrichters, um die Lampe zu starten durch Streichen über einen Betriebsfrequenzbereich, der mit einer vorbestimmten relativ hohen Anfangsfrequenz beginnt und die Betriebsfrequenz kontinuierlich senkt, bis die Ausgangsspannung ausreichend hoch ist, um die Lampe zu starten, und eine Überspannungs-Ausschalterschaltung, die mit der Lampe gekoppelt ist (32, 36), um den Wechselrichter immer dann für eine Überspannungs-Ausschaltperiode auszuschalten, wenn die Ausgangsspannung einen Ausschalt-Schwellenwert der Überspannung überschreitet, wobei die Wechselrichter-Steuerung nach der Überspannungs-Ausschaltperiode wieder Steuersignale liefert zum Überstreichen des Betriebsfrequenzbereiches, der mit der vorbestimmten relativ hohen Anfangsfrequenz beginnt, um die Lampe zu starten, **dadurch gekennzeichnet, daß** die Wechselrichter-Steuerung Steuersignale an die Gate-Treiberschaltung liefert, um den Betrieb nach dem Starten der Lampe über Resonanz zu halten, so daß der Ballast-Wechselrichter in einem im wesentlichen spannungslosen Schaltmodus arbeitet.

2. Ballastanordnung nach Anspruch 1, wobei der

Überspannungs-Ausschalt-Schwellenwert einen ersten Überspannungs-Ausschalt-Schwellenwert während des Lampenstartens und einen zweiten Überspannungs-Ausschalt-Schwellenwert während des normalen Laufbetriebs aufweist, wobei der erste Überspannungs-Ausschalt-Schwellenwert grösser als der zweite Überspannungs-Ausschalt-Schwellenwert ist, um ein Lampenstarten zu ermöglichen und einen Betrieb unter Resonanz zu verhindern.

3. Ballastanordnung nach Anspruch 1, wobei ferner eine Abtastvorrichtung (R3) vorgesehen ist zum Liefern eines Signals, das die Lichtabgabe von der Lampe anzeigt.
4. Ballastanordnung nach Anspruch 3, wobei die Abtastvorrichtung einen Stromsensor zum Abtasten von Strom in der Lampe aufweist.
5. Ballastanordnung nach Anspruch 4, wobei die Wechselrichter-Steuerung ein Pulsbreitenmodulations-Regelschaltung aufweist zum Empfangen eines Eingangs-Sollstroms (I*) und zum Vergleichen mit dem abgetasteten Lampenstrom.
6. Ballastanordnung nach Anspruch 1, wobei ferner eine Startverzögerungs-Zeitsteuerschaltung (40) vorgesehen ist zum Verzögern des Lampenstarts, bis ausreichend Zeit vergangen ist, um eine Erwärmung des Lampen-Glühfadens zu gestatten.
7. Ballastanordnung nach Anspruch 1, wobei die Überspannungs-Ausschalterschaltung eine Schaltvorrichtung (32) zum Verändern der Verstärkung der Ausgangsspannungs-Abtasterschaltung aufweist.
8. Ballastanordnung nach Anspruch 1, wobei die Ausschaltzeit der Steuersignale konstant und die Einschaltzeit der Steuersignale variabel ist, um die Frequenz und Amplitude der Ausgangsspannung aus dem Schalt-Wechselrichter zu verändern.

Revendications

1. Système de ballast pour au moins une lampe fluorescente atténuable (12, 22), comprenant :

un onduleur (10) de ballast pour exciter ladite au moins une lampe fluorescente afin de fournir une sortie de lumière, ledit onduleur de ballast comprenant un onduleur de commutation oscillant (L1, C4) pour fournir une tension de sortie servant à faire fonctionner ladite lampe ; un circuit pilote (13) de grille pour fournir des signaux de déblocage audit onduleur de com-

11

EP 0 818 129 B1

12

mutation oscillant sur une plage de fréquences ;

un contrôleur (11) d'onduleur pour fournir des signaux de commande audit circuit pilote de grille pour commander le fonctionnement oscillant dudit onduleur de ballast pour allumer la lampe en balayant une plage de fréquences de fonctionnement en commençant avec une fréquence initiale prédéfinie relativement élevée puis en réduisant de façon continue la fréquence de fonctionnement jusqu'à ce que ladite tension de sortie soit suffisamment élevée pour allumer ladite lampe ; et

un circuit de protection contre les surtensions couplé (32, 36) à ladite lampe pour éteindre ledit onduleur pendant une période d'arrêt de surtension à chaque fois que ladite tension de sortie dépasse un seuil d'arrêt de surtension, ledit contrôleur d'onduleur fournissant de nouveau des signaux de commande pour balayer la plage de fréquences de fonctionnement en commençant avec la fréquence initiale prédéfinie relativement élevée pour allumer la lampe après la période d'arrêt de surtension ; **caractérisé par le fait que**

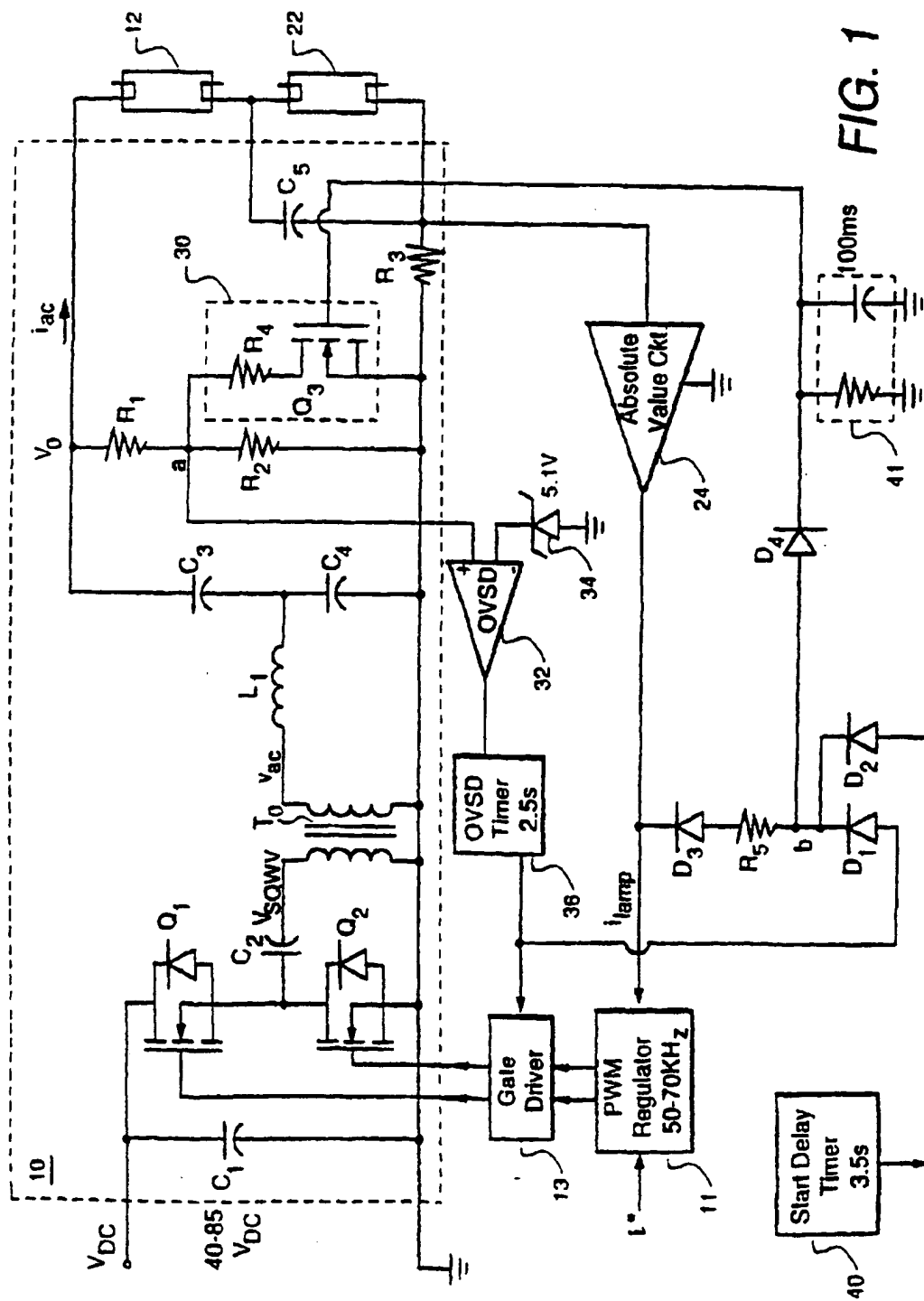
ledit contrôleur d'onduleur fournit des signaux de commande audit circuit pilote de grille pour maintenir le fonctionnement au-dessus de la résonance après allumage de la lampe de telle manière que ledit onduleur de ballast fonctionne dans un mode de commutation à tension sensiblement nulle.

2. Système de ballast selon la revendication 1, dans lequel ledit seuil d'arrêt de surtension comprend un premier seuil d'arrêt de surtension pendant l'allumage de la lampe et un deuxième seuil d'arrêt de surtension pendant le fonctionnement établi normal, ledit premier seuil d'arrêt de surtension étant supérieur audit deuxième seuil d'arrêt de surtension afin de permettre l'allumage de la lampe et d'éviter le fonctionnement sous la résonance.
3. Système de ballast selon la revendication 1, comprenant en outre un dispositif de mesure (R_3) pour fournir un signal représentant la lumière émise par ladite lampe.
4. Système de ballast selon la revendication 3, dans lequel ledit dispositif de mesure comprend un capteur de courant pour mesurer l'intensité dans ladite lampe.
5. Système de ballast selon la revendication 4, dans lequel ledit contrôleur d'onduleur comprend un circuit de régulateur MLI pour recevoir une instruction d'intensité d'entrée (i^*) et la comparer à l'intensité

de la lampe mesurée.

6. Système de ballast selon la revendication 1, comprenant en outre un circuit de temporisation de retard d'allumage (40) pour retarder l'allumage de ladite lampe jusqu'à ce qu'un temps suffisant ce soit écoulé pour permettre au filament de la lampe de chauffer.
7. Système de ballast selon la revendication 1, dans lequel ledit circuit de protection contre les surtensions comprend un dispositif de commutation (32) pour modifier le gain du circuit de mesure de tension de sortie.
8. Système de ballast selon la revendication 1, dans lequel le temps d'ARRÊT desdits signaux de déblocage est constant, le temps de MARCHE desdits signaux de blocage étant variable afin de faire varier la fréquence et l'amplitude de la tension de sortie provenant dudit onduleur de commutation.

EP 0 818 129 B1



EP 0 818 129 B1

